

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Per-Erik NORDAL et al. Attorney Docket No.: P67191US0

Serial No.: 09/926,531 Group Art Unit: 2831

Filed: November 15, 2001 Examiner: Hung V. NGO

For: VERTICAL ELECTRICAL INTERCONNECTIONS IN A STACK

AMENDMENT

BOX NON-FEE AMENDMENT.
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of March 21, 2003 (Paper No. 9), please amend the above-captioned application as follows:

IN THE CLAIMS:

Please amend claims 1-7 as follows:

1. (Amended) A memory and/or data processing device comprising

at least two stacked layers overlapping each other partially or completely, said layers being supported by a substrate or alternatively forming a sandwiched self-supporting structure of the stacked layers, and at least two layers in the stack including memory and/or processing circuitry connecting electrically to memory and/or processing circuitry in at least one other layer